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2 Analog circuits cookbook Figure 1.1(a) A parallel tuned circuit used as a rejector. The notch depth is set by the ratio of the tuned circuit's dynamic resistance R_d and the load resistance R_l . At F_0 the tuned circuit is equivalent to a resistance $R_d = Q\omega L$ (Q of capacitor assumed much larger). $F_0 = 1/2\pi\sqrt{LC}$. (b) The circuit modified to provide a deep

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So this second edition of the Analog Circuits Cookbook has been prepared, retaining roughly half of the articles which appeared in the first edition, and replacing the rest with other articles which have appeared more recently in Electronics World.

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